

**Alpha Bridge
AXFP-10G-SR
Datasheet**



Features

- Compliant with XFP-MSA INF-8077i
- Compliant with 10GFC 1200-MX-SN-I Fiber Channel Standard
- Compliant with IEEE802.3ae 10GBASE-SW/SR Ethernet Standard
- Differential CML inputs and CML outputs
- Single power supply 3.3V
- TTL signal detect indicator
- Hot Pluggable
- Class 1 laser product complies with EN 60825-1
- RoHS Compliant

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Note
Storage Temperature	T_s	-40	85	°C	
Supply Voltage	V_{cc3}	-0.5	4.0	V	
Input Voltage	V_{IN}	-0.5	V_{cc}	V	

Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Units	Note
Case Operating Temperature	T_c	-10	70	°C	
Supply Voltage	V_{cc3}	3.1	3.5	V	
Supply Current	I_{cc3}	---	400	mA	

Diagnostics

Data Address	Parameter	Range	Accuracy
96-97	Temperature	-10 °C to 85 °C	±3°C
100-101	Bias Current	0 to 100mA	±10%
102-103	TX Power	-8 to +0dBm	±2dB
104-105	RX Power	-15 to +0dBm	±2dB
106-107	Vcc3 Voltage	2.9~3.7 Volts	±3%

Transmitter Electro-optical Characteristics

V_{cc}=3.1 V to 3.5 V, T_c= -10°C to 70°C (-10°C to 85°C)

Parameter	Symbol	Min.	Type.	Max.	Units	Note
Operating Data Rate		9.95		11.3	Gbps	
Input Reference Clock Rate				Not required		
Output Optical Power (50/125µm fiber, NA=0.20) (62.5/125µm fiber NA=0.275)	P_{out}	-7.1	---	-1	dBm	
Optical Modulation Amplitude (OMA)	OMA	---	---	---	dBm	Refer to IEEE 802.3ae Table 52-3
Extinction Ratio	ER	3			dB	
Center Wavelength	λ_c	840		860	nm	
Spectral Width (RMS)	$\Delta\lambda$	---	---	---	nm	Refer to IEEE 802.3ae Table 52-8
Transmitter and Dispersion Penalty	TDP			3.9	dB	
Relative Intensity Noise	RIN	---	---	-128	dB/Hz	
Output Eye	Compliant with IEEE802.3ae					
Differential Input Voltage	V_{DIFF}	0.25	---	1.0	V	
TX_DISABLE Assert Time	t_{off}	---	---	10	µs	
TX_DISABLE Negate Time	t_{on}	---	---	2	ms	
Time to initialize	t_{init}	---	---	300	ms	
Interrupt negate delay	Interrupt_off			500	ms	
P_Down/RST assert delay	P_Down/RST_on			100	ms	

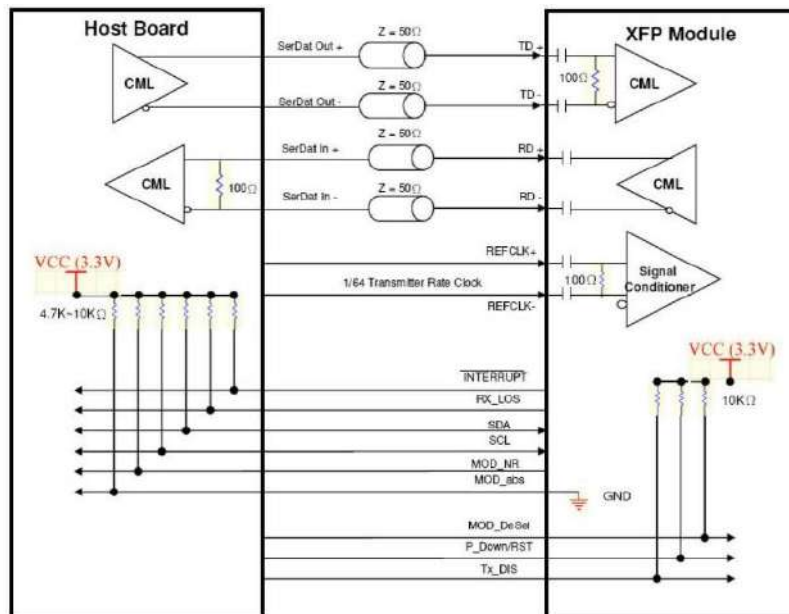
Mod_NR assert delay	Mod_nr_on			1	ms	
Mod_NR negate delay	Mod_nr_off			1	ms	
P-Down reset time		10			μ s	
RX_LOS assert delay	t_loss_on			100	μ s	
RX_LOS negate delay	t_loss_off			100	μ s	

Receiver Electro-optical Characteristics

Vcc=3.1 V to 3.5 V, Tc= -10°C to 70°C (-10°C to 85°C)

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Optical Input Power-maximum	P_{IN}	-1	---	---	dBm	BER < 10 ⁻¹²
Receiver Sensitivity	P_{IN}	---	---	-9.9	dBm	BER < 10 ⁻¹²
Receiver Sensitivity in OMA	P_{IN}	---	---	-11.1	dBm	BER < 10 ⁻¹²
Stress Receiver Sensitivity in OMA	P_{IN}	---	---	-7.5	dBm	BER < 10 ⁻¹²
Operating Center Wavelength	λ_c	840	---	860	nm	
Optical Return Loss	ORL	12	---	---	dB	
Loss of Signal-Asserted	P_A	---	---	-20	dBm	
Loss of Signal-Deasserted	P_D	-12	---	---	dBm	
Differential Output Voltage	V_{DIFF}	0.575	---	0.725	V	
TTL Input High Voltage		2		Vcc	V	
TTL Input Low Voltage		0		0.8	V	
TTL output High Voltage		2.4	---	Vcc	V	
TTL output Low Voltage		0	---	0.4	V	
Receiver Loss of Signal Assert Time (off to on)	t_{A,RX_LOS}	---	---	100	μ s	
Recover Loss of Signal Assert Time (on to off)	t_{D,RX_LOS}	---	---	100	μ s	
I2C Clock Frequency	fSCL			400	kHz	

Block Diagram of Transceiver



This product converts the 4-channel of 100Gbps aggregated NRZ electrical input data into one channel of 50Gbaud PAM4 optical signal (light) on 1310nm center wavelength through a DSP based gearbox, by a driven cooled Electro-absorption Modulated DFB Laser (EML). The light propagates out of the transmitter into an SMF fiber. The receiver module accepts the 50Gbaud PAM4 optical signal input, and converts it into a 50Gbaud PAM4 electrical signal via a linear amplifier. And then convert the 50Gbaud PAM4 signal

into 4 channels of 25Gbps NRZ signals. Above figure shows the functional block diagram of this product.

MOD_NR

The Mod_NR is an output pin that when High, indicates that the module has detected a condition that renders transmitter and or receiver data invalid, shall consist of logical OR of the following signals:

- Transmit Signal Conditioner Loss of Lock
- Transmitter Laser Fault
- Receiver Signal Conditioner Loss of Lock

MOD_DESEL

The Mod_DeSel is an input pin. When held Low by the host, the module responds to 2-wire serial communication commands. The Mod_DeSel allows the use of multiple XFP modules on a single 2-wire interface bus. When the Mod_DesSel pin is “High”, the module shall not respond to or acknowledge any 2-wire interface communication from the host.

INTERRUPT

Interrupt is an output pin. When “Low”, indicates possible module operational fault or a status critical to the host system.

TX_DIS

TX_DIS is an input pin. When TX_DIS is asserted High, the XFP module transmitter output must be turned off.

MOD_ABS

Mod_ABS is pulled up to Host_Vcc on the host board and grounded in the XFP module. Mod_ABS is then asserted “High” when the XFP module is physically absent from a host slot.

RX_LOS

The RX_LOS when High indicates insufficient optical power for reliable signal reception.

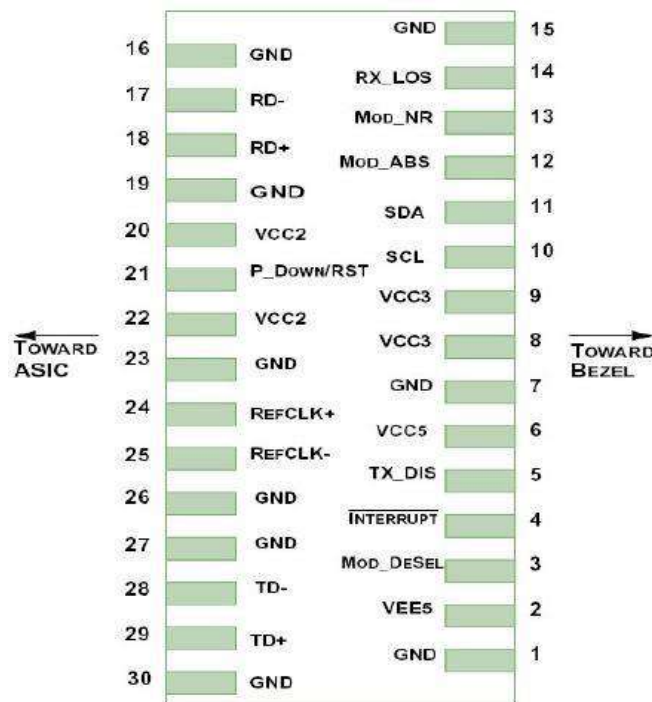
P_DOWN/RST

This is a multifunction pin for module Power Down and Reset. The P_Down/RST pin must be pulled up to VCC3 in the XFP module.

POWER DOWN FUNCTION

The P_Down pin, when held high by the host, places the module in the standby (Low Power) mode with a maximum power dissipation of 1.5W. This protects hosts which are not capable of cooling higher power modules which may be accidentally inserted.

Pin Assignment



Pin Description

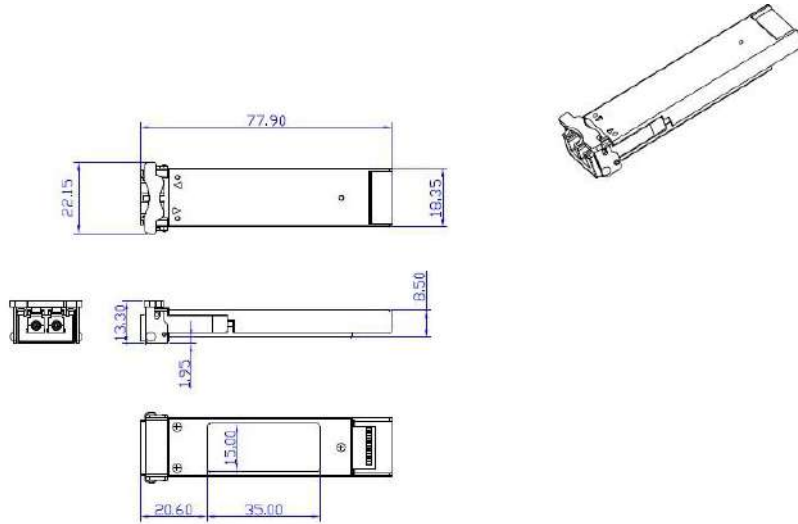
PIN	Logic	Symbol	Name/Description	Note
1		GND	Module Ground	1
2		VEE5	Optional -5.2V Power Supply	

3	LVTTTL-I	Mod_DeSel	Module De-select; When held low allows module to respond to 2-wire serial interface	
4	LVTTTL-O	Interrupt	Interrupt; Indicates presence of an important condition which can be read over the 2-wire serial interface	2
5	LVTTTL-I	TX_DIS	Transmitter Disable; Turns off transmitter laser output	
6		VCC5	+5V Power Supply	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTTL-I/O	SCL	2-Wire Serial Interface Clock	2
11	LVTTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
12	LVTTTL-O	Mod_Abs	Indicates Module is not present. Grounded in the Module	2
13	LVTTTL-O	Mod_NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTTL-O	RX_LOS	Receiver Loss of Signal Indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver Inverted Data Output	
18	CML-O	RD+	Receiver Non-Inverted Data Output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply	3
21	LVTTTL-I	P_Down/RST	Power down; When high, requires the module to limit power consumption to 1.5W or below, 2-Wire serial interface must be functional in the low power mode.	
			Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply	3
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Not Required	
25	PECL-I	RefCLK-		
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter Inverted Data Input	
29	CML-I	TD+	Transmitter Non-Inverted Data Input	
30		GND	Module Ground	1

Notes:

1. Module ground pins Gnd are isolated from the module case and chassis ground within the module.
2. Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.
3. The 1.8V power supply can be optionally programmed to voltages lower than 1.8V in modules supporting the variable power supply.

Dimensions

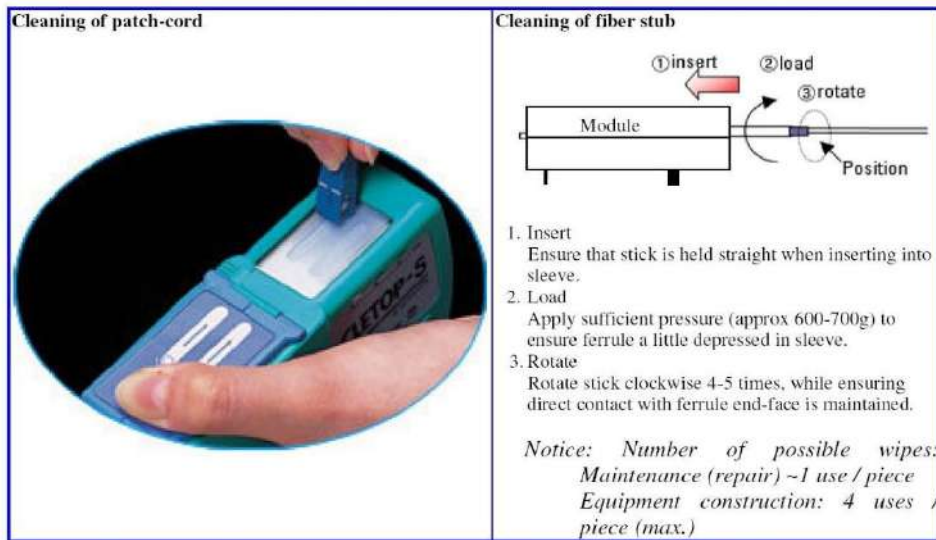


DIMENSIONS ARE IN MILLIMETERS

ALL DIMENSIONS ARE ±0.2mm UNLESS OTHERWISE SPECIFIED

Optical Receptacle Cleaning Recommendations

All fiber stubs inside the receptacle portions were cleaned before shipment. In the event of contamination of the optical ports, the recommended cleaning process is the use of forced nitrogen. If contamination is thought to have remained, the optical ports can be cleaned using a NTT international Cletop[®] stick type and HFE7100 cleaning fluid. Before the mating of patch-cord, the fiber end should be cleaned up by using Cletop[®] cleaning cassette.



Note: The pictures were extracted from NTT-ME website. And the Cletop[®] is a trademark registered by NTT-ME

Ordering Information

Model Number	Part Number	Reach	Input/Output	Signal Detect	Voltage	Temperature
AXFP-10G-SR	OP7K-MX3-85-C	300 m	AC/AC	TTL	3.3V	-10°C to 70 °C
AXFP-10G-SR-I	OP7K-MX3-85-I	300 m	AC/AC	TTL	3.3V	-10°C to 85°C

